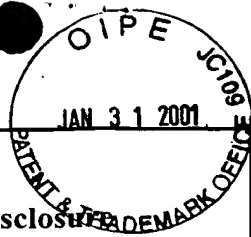


**Form 1449 (Modified)****Information Disclosure  
Statement By Applicant**

(Use Several Sheets if Necessary)

Atty Docket No.  
CISCP035/384  
Applicant:  
FINDLATER *et al.*  
Filing Date  
June 2, 1998Application No.:  
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2661**RECEIVED**

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**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>qntw</i>	A1	5,469,434	11/21/95	Kurdzo <i>et al.</i>			01/21/93
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**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	A7							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	A8	Internal Cisco documentation re: sold Ethernet switch, entitled, "CHAINSAW PCB: Internal Design Specification", January and July 1996.
	A9	Advanced Micro Devices, "Quad Ethernet Switching Transceiver (QuEST™)", Am79C989, Publication No. 21173, April 1997
	A10	Texas Instruments, "Eight 10Base-T Physical-Layer Interfaces", TNETE2008, December 1997, Revised February 1998.
Examiner <i>qntw</i>		Date Considered 5/18/01

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.